

Substitute for form PTO-1449  <b>INFORMATION DISCLOSURE STATEMENT</b>				Application No.	10/684,053
				Filing Date	OCTOBER 9, 2003
				First Named Inventor	MATHUR
				Art Unit	2183
				Examiner Name	HUISMAN
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### US PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document No.	Document Date	Applicant/Patentee Name	Relevant Pages, Columns, Lines, Passages, or Figures
	AA	2001/0014937	08/16/2001	Huppenthal et al.	
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	AG	2004/0019771	01/29/2004	Quach	
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	AII	5931959	08/03/1999	Kwiat	
	AJJ	5933356	08/03/1999	Rostoker et al.	

 Examiner  
Signature

/David Huisman/

Date Considered

10/16/2008

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	APPP				
	AQQQ				
	ARRR				
	ASSS				
	ATTT				
	AUUU				

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### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Country Code and Document No.	Document Date	Applicant/Patentee Name	Relevant Pages, Columns, Lines, Passages, or Figures	English Transl?
	AVVV	JP2002-149424	05/24/2002	International Business Machines		
	AWWW					
	AXXX					
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Examiner Initials*	Cite No.	Citation	English Transl?
	AHHHH	Free Online Dictionary of Computing (1997) Definitions of "FPGA" and "raw data"; <a href="http://foldoc.org">http://foldoc.org</a>	
	AIIII	International Search Report for International Patent Application No. PCT/US2005/035813	
	AJJJJ	International Search Report for International Patent Application No. PCT/US2005/035814	
	AKKKK	Microsoft (2002) Microsoft Computer Dictionary, 5 <sup>th</sup> Edition; Microsoft Press; pp. 77-78	
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